

SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)
Seshadri Rao Knowledge Village, Gudlavalleru – 521356, Krishna District (A.P.)

Examination Section

II B.Tech - I Semester (R23) Supplementary Examinations, June 2025

Timings - 10.00 am to 01.00 pm

TIME TABLE

22nd May 2025

DATE & DAY	CE	EEE	ME	ECE	CSE	IT	AI&DS	IOT	AI&ML
09.06.2025 (Monday)	Surveying (CE4504)	Universal Human Values Understanding Harmony and Ethical Human Conduct (UH4501)	Numerical Methods and Transform Techniques (MA4504)	Universal Human Values Understanding Harmony and Ethical Human Conduct (UH4501)	--	Probability and Statistics (MA4506)	Database Management Systems (CT4517)	Complex Variables and Probability Distributions (MA4507)	Probability and Statistics (MA4506)
10.06.2025 (Tuesday)	Managerial Economics and Financial Analysis (BA4501)	Complex Variables and Numerical Methods (MA4503)	Thermodynamics (ME4503)	Managerial Economics and Financial Analysis (BA4501)	Digital Logic and Computer Organization (CT4510)	Digital Logic and Computer Organization (CT4510)	Managerial Economics and Financial Analysis (BA4501)	Analog Devices and Circuits (EC4502)	Artificial Intelligence (CT4516)
11.06.2025 (Wednesday)	Strength of Materials (CE4505)	Electromagnetic Field Theory (EE4507)	Mechanics of Solids (ME4504)	Probability Theory and Stochastic Processes (EC4506)	Advanced Data Structures and Algorithm Analysis (CT4511)	Advanced Data Structures and Algorithm Analysis (CT4511)	Advanced Data Structures and Algorithm Analysis (CT4511)	Introduction to Python (CT4509)	--
12.06.2025 (Thursday)	Fluid Mechanics (CE4506)	Electrical Circuit Analysis-II (EE4508)	Material Science and Metallurgy (ME4505)	Signals and Systems (EC4507)	Object Oriented Programming Through Java (CT4512)	Object Oriented Programming Through Java (CT4512)	Object Oriented Programming Through Java (CT4512)	Signal Processing and Communications (EC4503)	Object Oriented Programming Through Java (CT4512)
13.06.2025 (Friday)	Design Thinking & Innovation (DT4501)	DC Machines and Transformers (EE4509)	Design Thinking & Innovation (DT4501)	Electronic Devices and Circuits (EC4508)	Discrete Mathematics and Graph Theory (MA4505)	Design Thinking & Innovation (DT4501)	Discrete Mathematics and Graph Theory (MA4505)	Design Thinking & Innovation (DT4501)	--

Note: Seating arrangement will be notified on the day of each examination half an hour before the commencement of the examination.


Controller of Examinations

Copy to: All Notice Boards

HoDs – CE/EEE/ME/ECE/CSE/IT/AI&DS/IoT/AI&ML/BS&H/ENG/MBA/CoE

Tables of Principal/Vice-Principal (Academics)/Vice-Principal (Administration)